Xilinx

Fpga.googoolia.com

Verilog quick start book

پاس کتابهای سایت saeed

Lab On a Chip

System On a Chip

Verilog, VHDL, System-C

Synthesizer

Software:

Active HDL from Aldec for starters

ModelSim from Mentor for serious projects

IUS from cadence in linux (50000 dallar per year)

VCS from synopsis in linux

SystemC

Micro blaze cpu and linux

ISE from Xilinix

Synplify synthesizer